



Reply under 37 CFR 1.116
– Expedited Procedure –
Technology Center 2816

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANT: CHOI, Jong-Hyun ART UNIT: 2816
SERIAL NO: 10/726,095 EXAMINER: ENGLUND, Terry Lee
FILED: December 2, 2003 DOCKET: 8021-172 (SS-18277-US)
ON: REFERENCE VOLTAGE GENERATING CIRCUIT AND
INTERNAL VOLTAGE GENERATING CIRCUIT FOR CONTROLLING
INTERNAL VOLTAGE LEVEL

OK TO ENTER
FOR ALLOWANCE
1.17.06
TLE

Mail Stop AF
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL UNDER 37 CFR § 1.116

This paper is responsive to the Final Office Action mailed on November 2,
2005. The Examiner is requested to enter and consider the following

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence (and any document referred to as being
attached or enclosed) is being deposited with the United States Postal Service as first class mail,
postage paid in an envelope addressed to: Commissioner for Patents, PO Box 1450,
Alexandria, VA 22313-1450 on the date indicated below:

Date: _____

By: _____

Susan A. Guarascio